

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Akiharu MIYANAGA et al.	Art Unit :	2818
Serial No. :	10/667,899	Examiner :	David Vu
Filed :	September 23, 2003	Conf. No. :	8644
Title :	SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME		

**MAIL STOP RCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Please consider the references listed on the enclosed PTO-1449 form. Foreign patent documents and non-patent literature are enclosed; cited U.S. patents and patent application publications will be provided on request.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: July 24, 2009

  
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